

In the Figures:

Applicants add Figure 2A to address the Examiner's objection to the drawings. Specifically, Figure 2A depicts a rewritable memory and a non-rewritable memory implemented in a single memory. Applicants attach hereto a new Figure 2A.

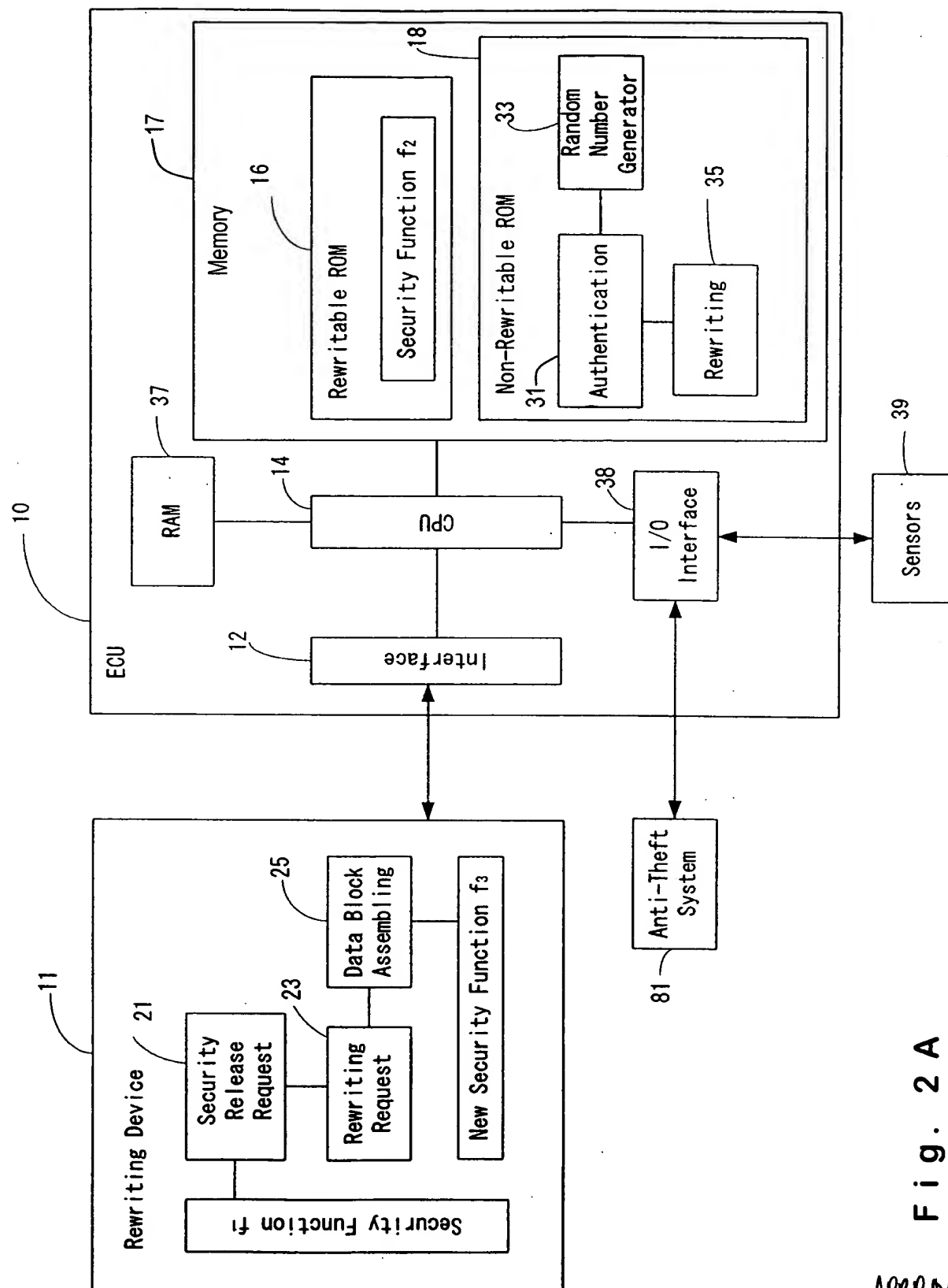
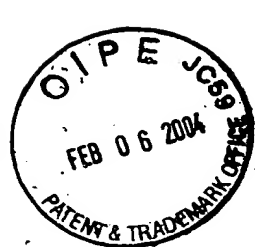


Fig. 2 A

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